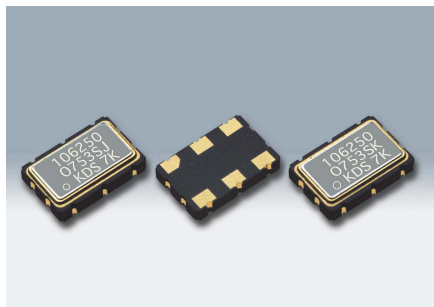


SMD Crystal Oscillators

DS0753S SERIES



Actual size

Features

- Package size: 7.3×4.9×1.5mm
- 3.3V operating voltage, High speed type (100~160MHz)
- 3-state function
- LV-PECL output (DS0753SK)
- LVDS output (DS0753SJ)

Applications

- Sever, FC-HBA

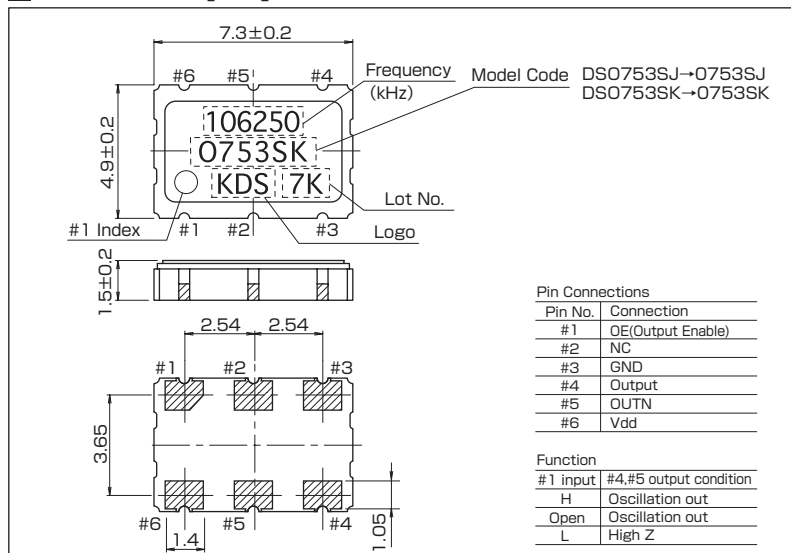


Standard Specification

Item	Type	Legend	DS0753SK	DS0753SJ
Output Specification		–	LV-PECL	LVDS
Output Frequency Range	f_o			100~160MHz
Supply Voltage	V _{dd}			+3.3±0.165V
Storage Temperature Range	T _{stg}			-40~+85°C
Operating Temperature Range	T _{use}			-10~+70°C
Frequency Tolerance	f _{tol}			±50×10 ⁻⁶ max.
Current Consumption	I _{dd}		80mA max.	60mA max.
Load Resistance	Load-R		50Ω to V _{dd} -2V	100Ω Output-OUTN
Symmetry	SYM		45~55% [at outputs cross point]	
Rise and Fall Time	tr, tf		0.7ns max. [20~80% Output Level]	
0 Level Output Voltage	V _{ol}		V _{dd} -1.81~V _{dd} -1.62V	–
1 Level Output Voltage	V _{oh}		V _{dd} -1.025~V _{dd} -0.88V	–
Differential Output Voltage	V _{OD1} , V _{OD2}		–	0.247~0.454V
Change to V _{OD}	ΔV _{OD}		–	50mV [ΔV _{OD} = V _{OD1} -V _{OD2}]
Offset Voltage	V _{OS1} , V _{OS2}		–	1.125~1.375V
Offset to V _{OS}	ΔV _{OS}		–	50mV [ΔV _{OS} = V _{OS1} -V _{OS2}]
Output Enable Time	T _{pZL}		2ms	
Output Disable Time	T _{pLZ}		200ns	
OE Pin 0 Level input Voltage	V _{il}		V _{dd} ×0.3max.	
OE Pin 1 Level input Voltage	V _{ih}		V _{dd} ×0.7min.	
Packing Unit			1000pcs./reel (φ254)	

Consult our sales representative for other specifications.

Dimensions[mm]



Recommended Land Pattern [mm]

