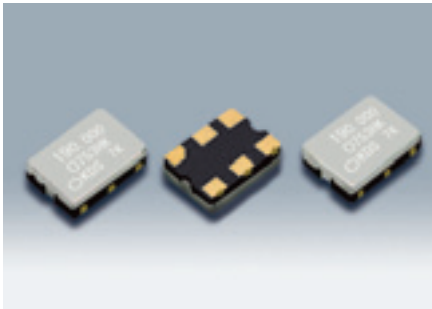


# SMD Crystal Oscillators

## DS0753H SERIES



Actual size

### ■ Features

- Available with three types of output functions: CMOS, PECL, LVDS
- Differential output (PECL, LVDS)
- Low jitter
- Tri-state function

### ■ Applications

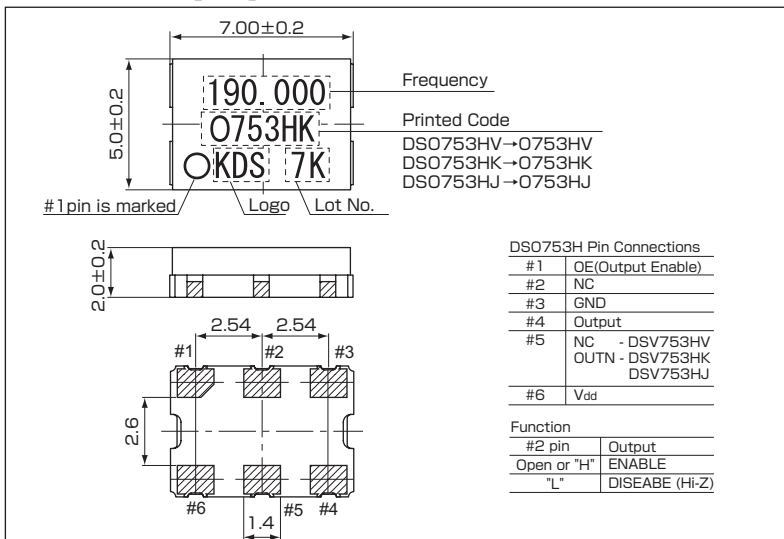
- Optical transmission device, radio transmitter-receiver equipment

### ■ Standard Specification

Item	Type	Legend	DS0753HV	DS0753HK	DS0753HJ
Output Specification	—		CMOS	LV-PECL	LVDS
Output Frequency Range	fo		150~230MHz	160~230MHz	160~230MHz
Supply Voltage	Vdd		+3.3V±0.165V		
Storage Temperature Range	T_stg		-40°C~85°C		
Operating Temperature Range	T_use		-20°C~70°C		
Frequency Tolerance	f_tol		±50×10 <sup>-6</sup> max.		
Current Consumption	Idd		50mA max.	80mA max.	60mA max.
Output Load	L_CMOS		15pF max.	50Ω to Vdd-2V	100Ω Output-OUTN
Symmetry	SYM		45%~55%[50% Vdd Level]	45%~55%[at 2.0V Level] 45%~55%[at outputs cross point]	45%~55%[at outputs cross point]
Rise and Fall Time	tr, tf		2.0ns max.[10%~90% Vdd Level]	1.0ns max.[20%~80% Output Level]	1.0ns max.[20%~80% Output Level]
0 Level Output Voltage	Vol		Vdd×0.1 max.	Vdd-1.025V~Vdd-0.88V	—
1 Level Output Voltage	Voh		Vdd×0.9 min.	Vdd-1.81V~Vdd-1.62V	—
Differential Output Voltage	VOD1, VOD2		—	—	0.247V~0.454V
Change to VOD	ΔVOD		—	—	50mV[ΔVOD=   VOD1-VOD2   ]
Offset Voltage	VOs1, VOs2		—	—	1.125V~1.375V
Change to VOs	ΔVOs		—	—	100mV[ΔVOs=   VOs1-VOs2   ]
Output Enable Time	Tpzl		2ms max.		
Output Disable Time	Tplz		200ns max.		
OE Pin 0 Level Input Voltage	Vil		Vdd×0.7 min.		
OE Pin 1 Level Input Voltage	Vih		Vdd×0.3 max.		
Packing Unit			100pcs. / reel or 500pcs. / reel (φ 180)		

Consult our sales representative for other specifications.

### ■ Dimensions[mm]



### ■ Recommended Land Pattern [mm]

